Building Blocks for the Wireless Internet Economy

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August 23, 2000

Intel Developer Forum
Fall 2000

Agenda

- Wireless Access to the Internet
- Building Blocks to Meet the Need
- New Application Development Paradigm

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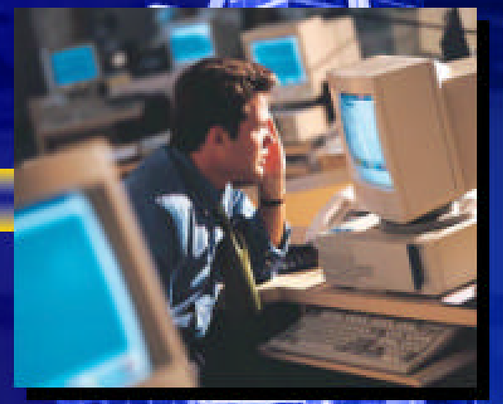
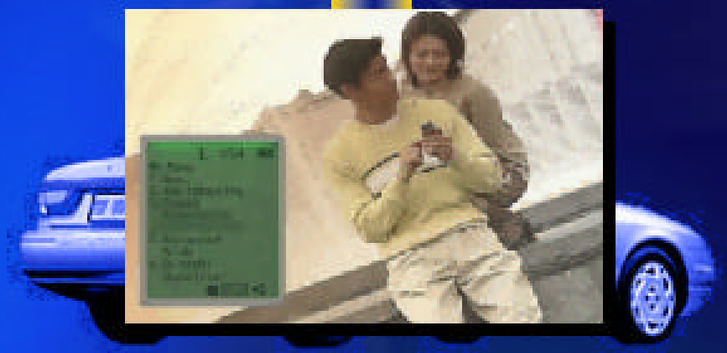
The Internet

e-Home



e-Business



e-Everywhere

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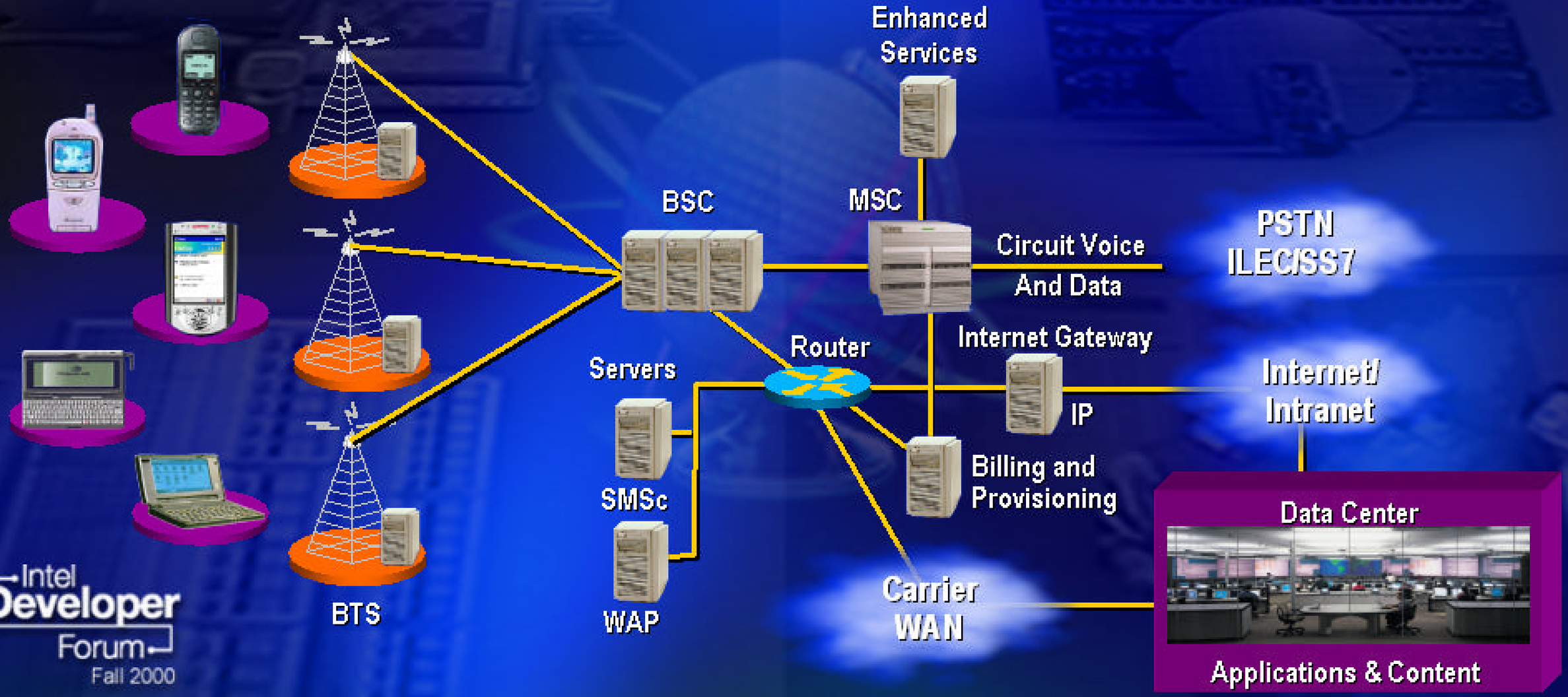
Wireless Access to the Internet



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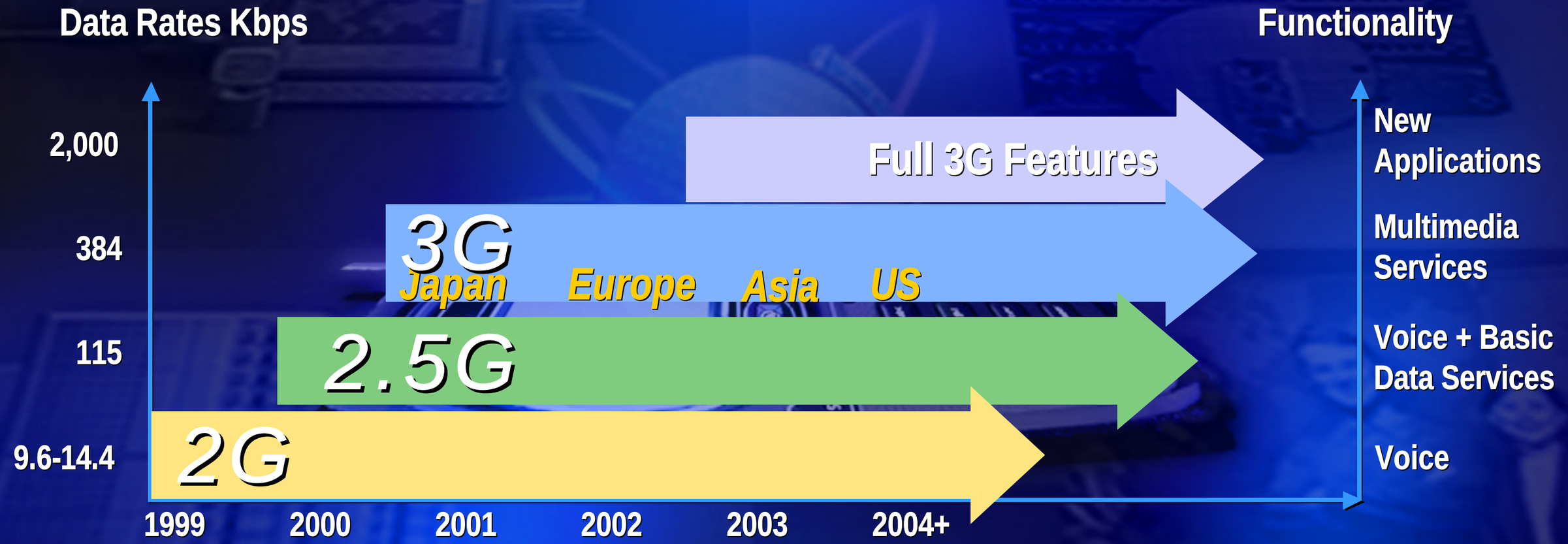
Cellular Transition



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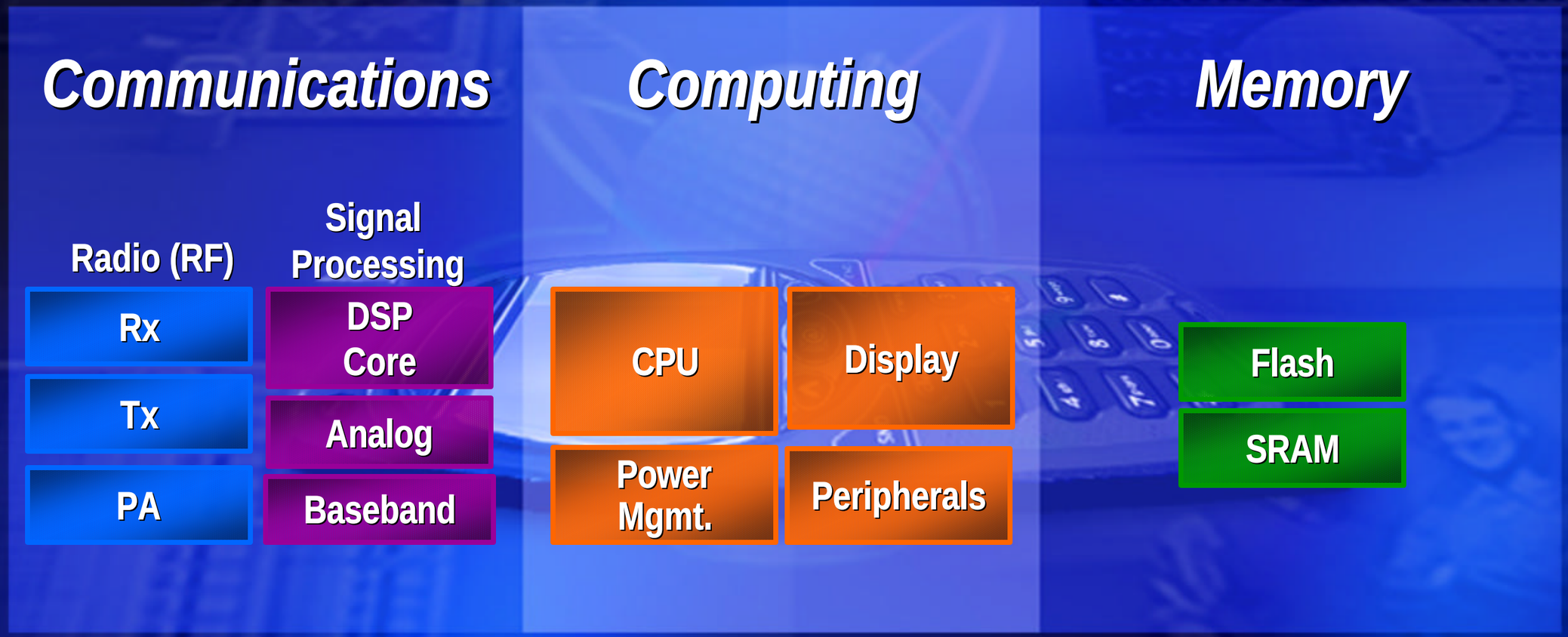
Transition from Voice → Voice + Data Drives New Requirements

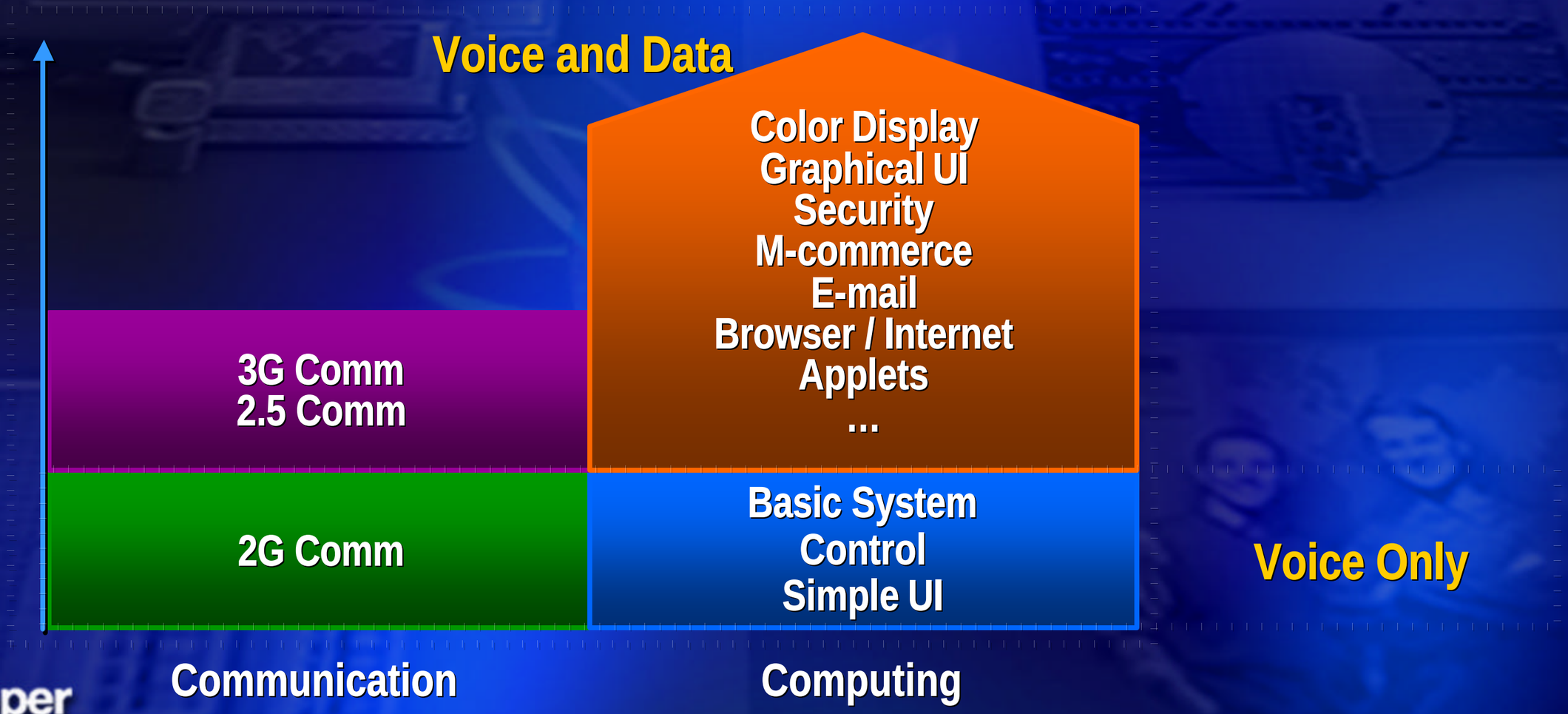
Infrastructure Requirements

- Increasing performance while maintaining low power
- Open, general purpose, and programmable to add new applications
- Scalable for build-out

Today's Wireless Terminal



Internet Use Drives Changes



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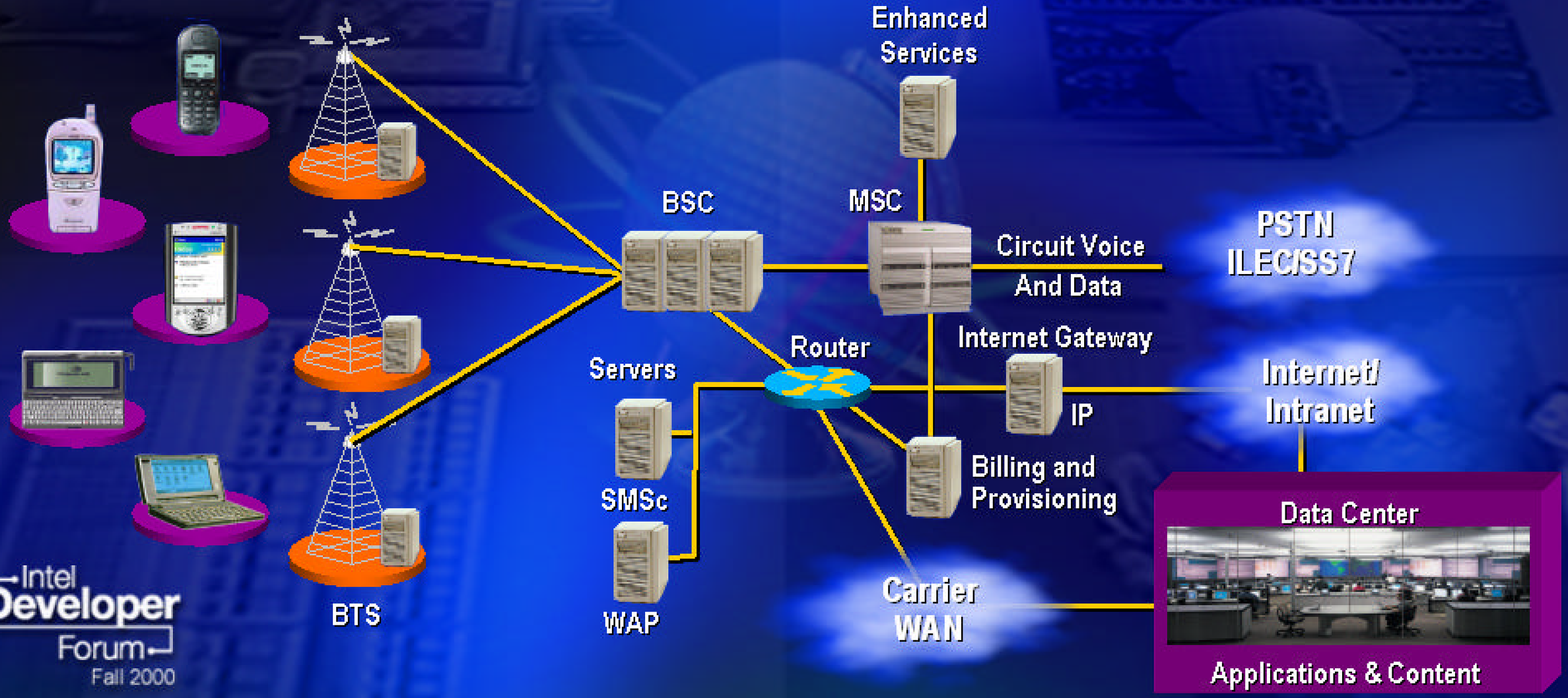
Intel® Building Blocks



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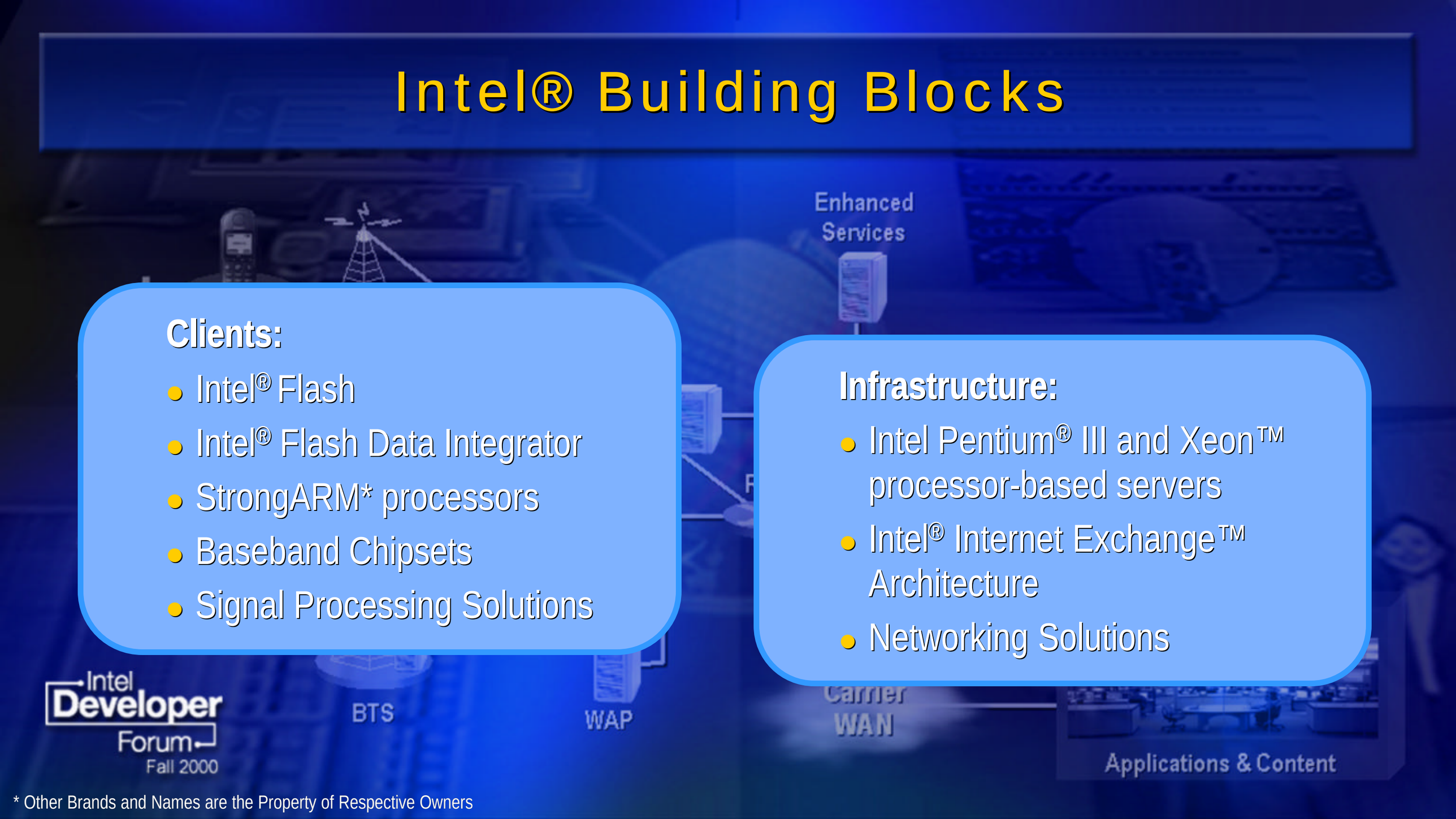
Intel® Building Blocks



Clients:

- Intel® Flash
- Intel® Flash Data Integrator
- StrongARM* processors
- Baseband Chipsets
- Signal Processing Solutions

Infrastructure:

- Intel Pentium® III and Xeon™ processor-based servers
- Intel® Internet Exchange™ Architecture
- Networking Solutions

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* Other Brands and Names are the Property of Respective Owners

Introducing: Intel® XScale™ Microarchitecture

- Tailored to address handheld clients and internet infrastructure
- Delivers leading mW/MIPs performance
- Enhanced from the Intel® StrongARM* microarchitecture
- Extends Intel's portfolio of computing solutions
  - Integrated into multiple product roadmaps from handheld Internet devices to Internet infrastructure

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DEMO

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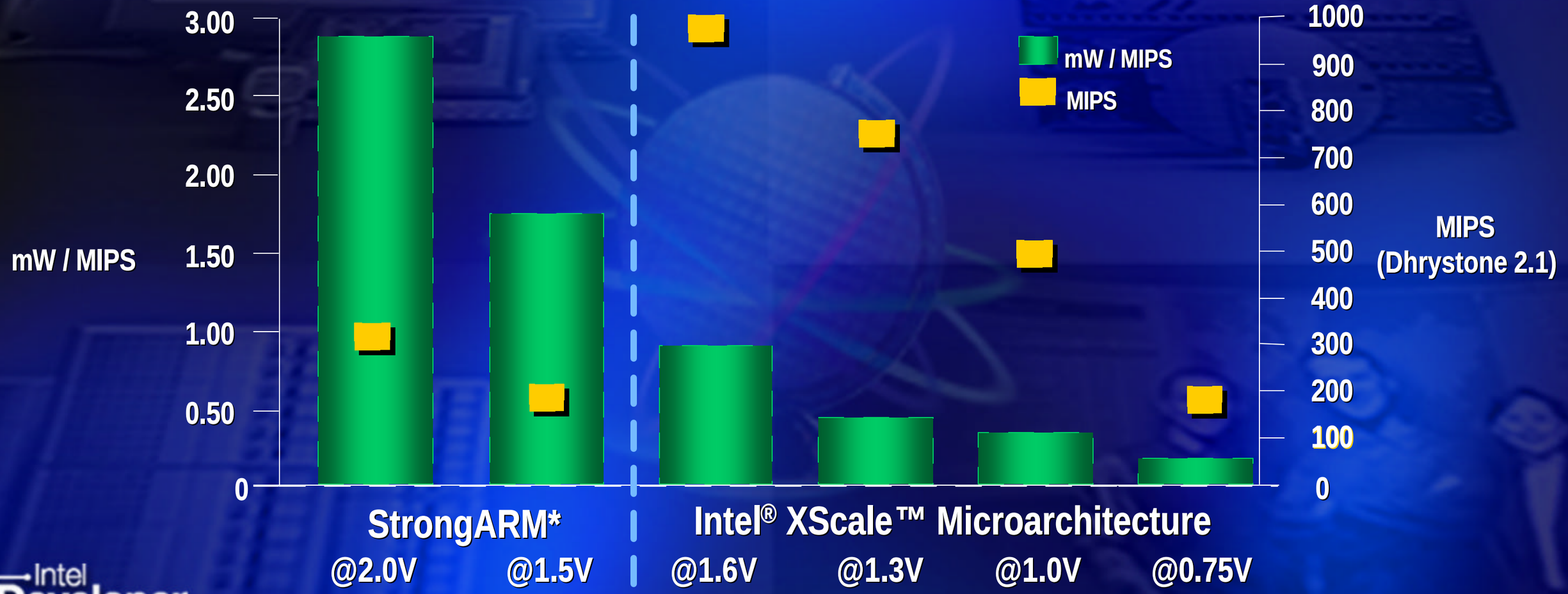
Intel® XScale™ Microarchitecture Power/Performance



125MIPs at 20mW

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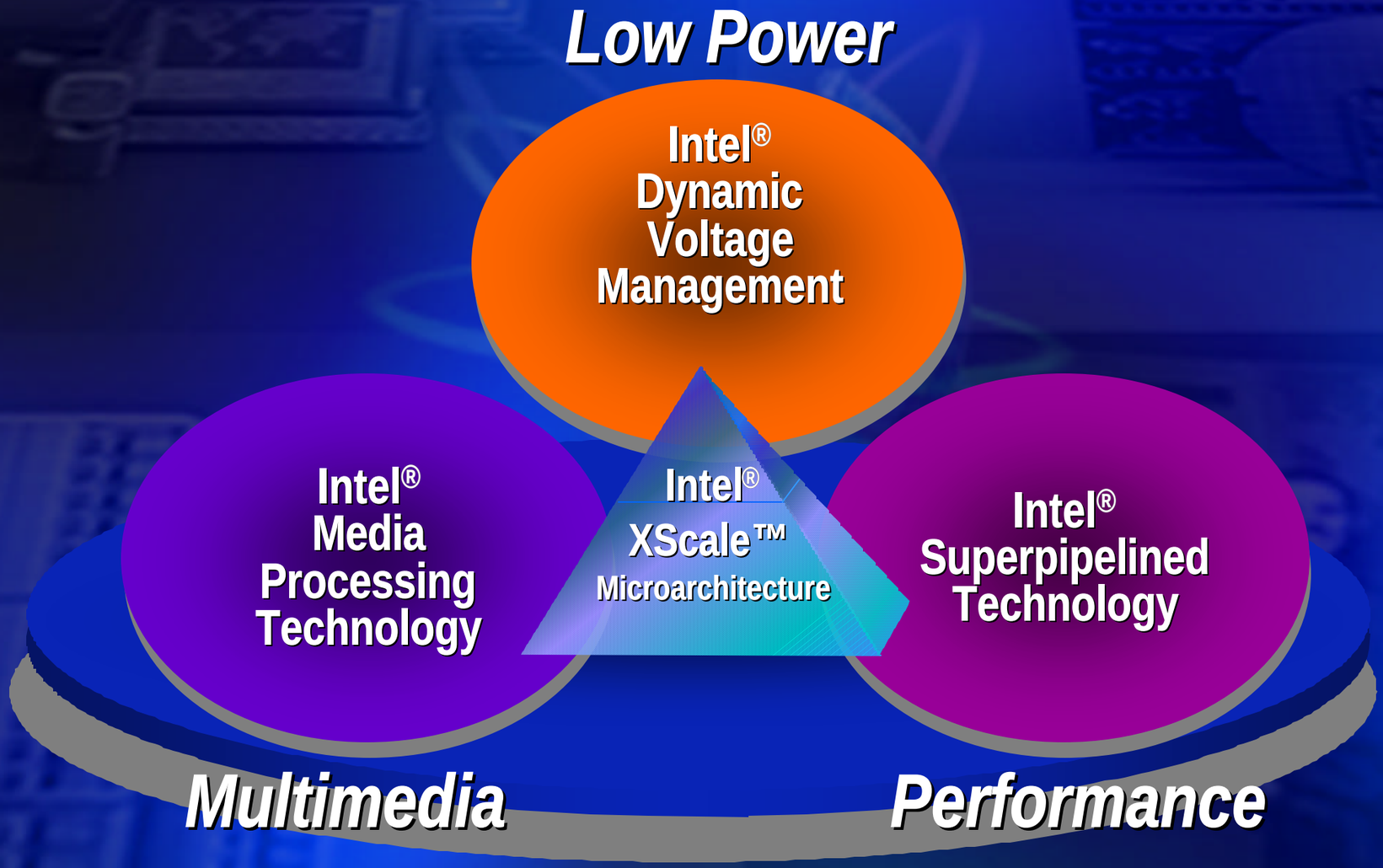
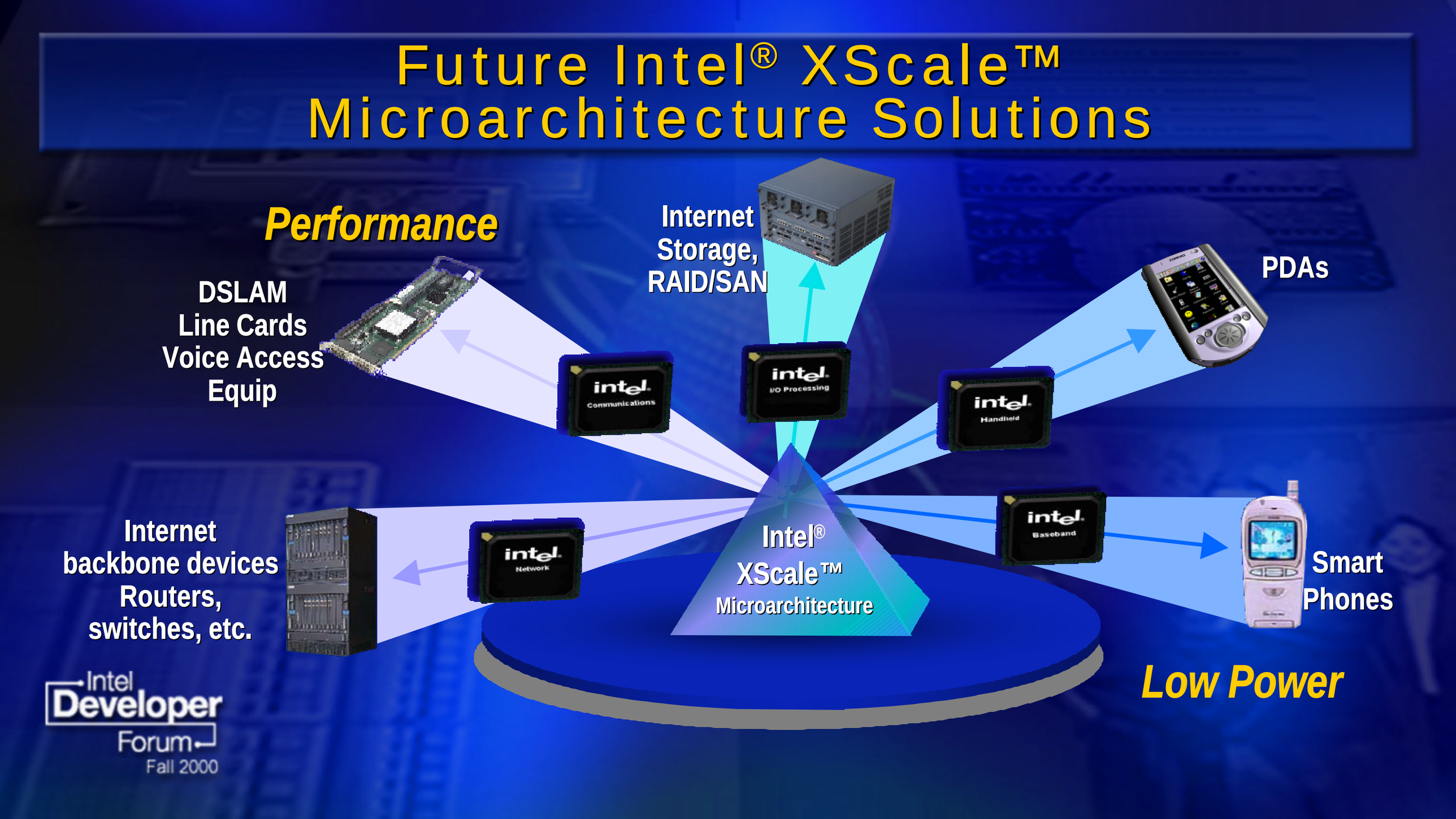
Source: Intel

Intel® XScale™ Microarchitecture Enhancements



Future Intel® XScale™ Microarchitecture Solutions



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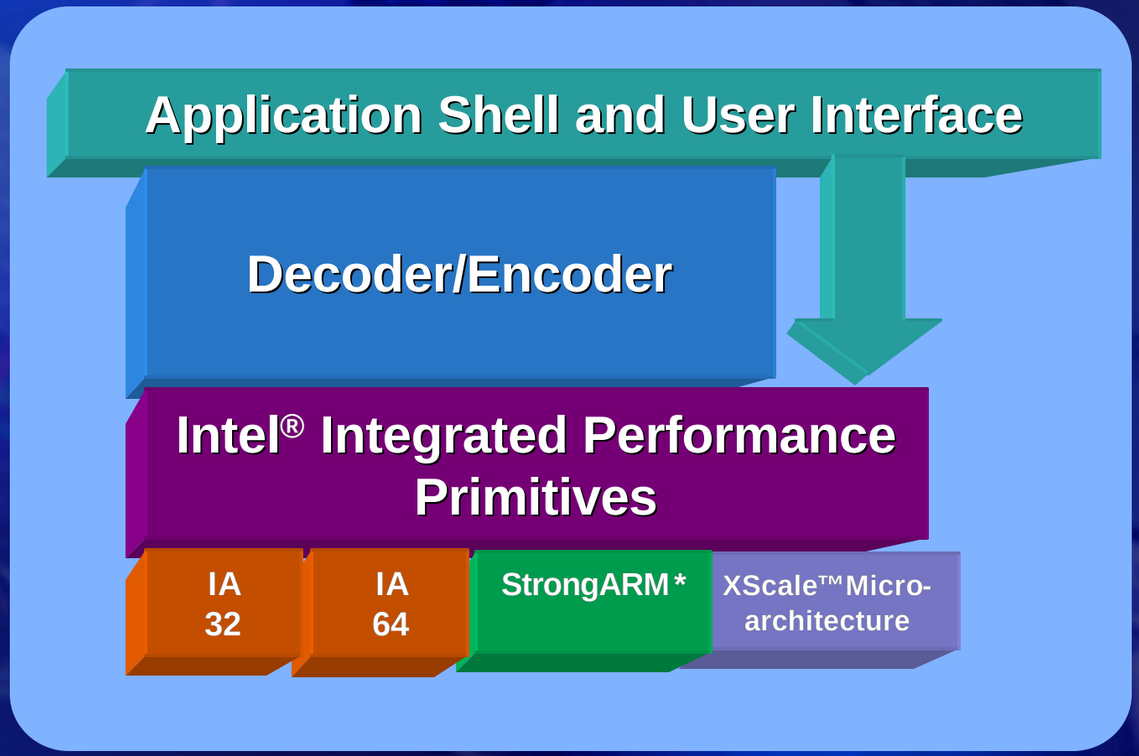
Intel® Integrated Performance Primitives

- **Pre-optimized libraries of code to speed time-to-market**
  - Improved audio, video and graphics
  - Headroom for performance-intensive applications
  - OS Independent
- **Available from Intel in 4Q 2000**



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Key Building Blocks for Multimedia Application Development

Intel® XScale™ Microarchitecture

Optimized for Both Ultra Low Power and High Performance

Low Power Technology & Markets

Intel® XScale™ Microarchitecture

High Performance Technology & Markets

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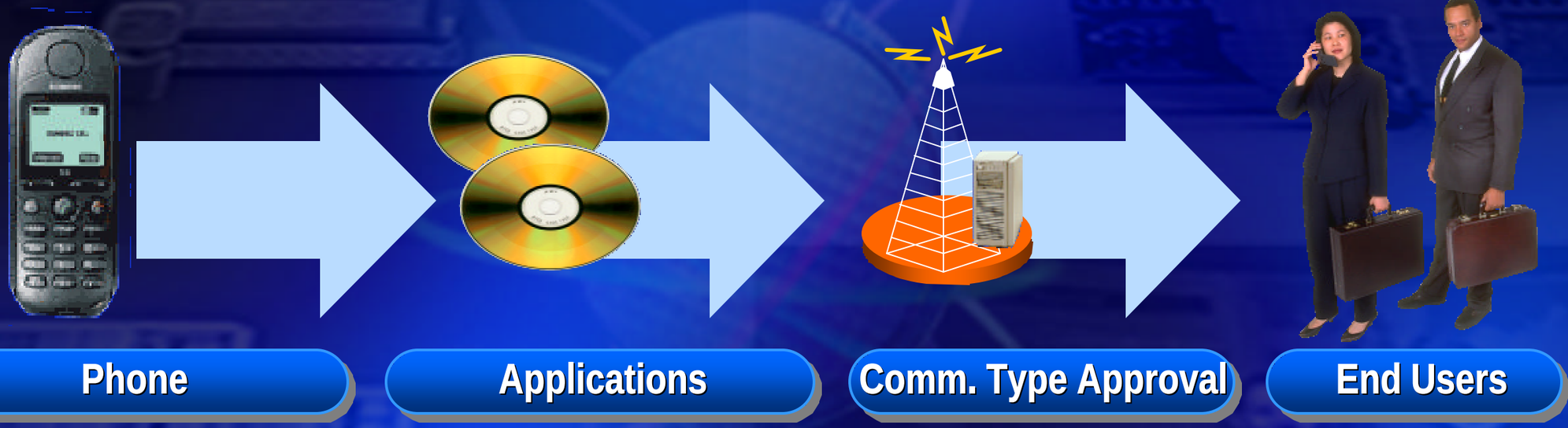
- New Building Blocks Enable
- New Application Development Paradigm

Application Delivery Today



- Serial development
- Applications require type approval
- Long pipeline for application and device delivery

Parallel Development Tomorrow

Communication

Clients

Applications

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Independent Compute and Communication Stacks

Compute

- CPU
- Power Management
- Display
- Peripherals
- USB
- Bluetooth*
- Flash
- RAM

Communications

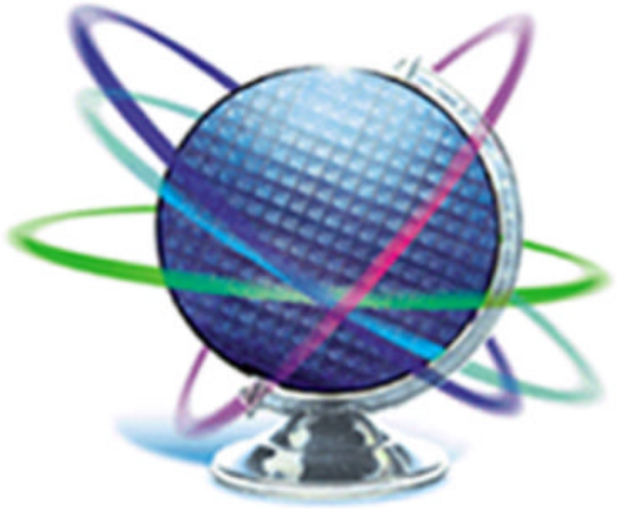
- Rx
- Tx
- PA
- CPU
- Analog
- DSP
- Flash
- SRAM

Intel® XScale™ Microarchitecture Enables New Paradigm

- Scalable via applications written to a general purpose processor
  - Leading power/performance
- Applications independent of communication stack

Summary: Your Opportunity

- Wireless access to the Internet brings opportunity:
  - New devices (hardware)
  - New applications (software)
- Intel® XScale™ Microarchitecture along with other Intel products enable this transition and the new application development paradigm



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